

Quantum Circuit Analysis with Model Counting (#SAT)

Santiago $\longleftrightarrow$ Leiden 2026 • Lecture 2 of 3

Alfons Laarman

Leiden University
Santiago 2026



**Universiteit
Leiden**
The Netherlands

Santiago 2026 — a three-lecture series

1. Demystifying Quantum Computing
2. Quantum Circuit Analysis with #SAT
3. Knowledge Representation for Quantum Computing

quantum computing 101

model counting

knowledge representation

Classical SAT solvers already crack open problems in math

MATHEMATICS

Maths proof smashes size record

Supercomputer produces a 200-terabyte proof — but is it really mathematics?

BY EVELYN LANG

Three computer scientists have announced the largest-ever mathematical proof: a file that comes in at a whopping 200-terabyte, equivalent to all the digital text held by the US Library of Congress. The researchers have created a 49-gigabyte compressed version of their solution — which would allow anyone with about 30,000 hours of spare processor time to download, reconstruct and verify it — but a human could never hope to read through it.

Computer-aided proofs too large to be directly verifiable by humans have become common, as have computers that solve problems in combinatorics — the study of finite discrete structures — by checking through enormous individual cases. Still, “200-terabyte is unbelievable,” says Ronald Graham, a mathematician at the University of California, San Diego. The previous record-holder is thought to be a 15-gigabyte proof*, published in 2014. The puzzle that required the 200-terabyte proof, called the Boolean Pythagorean triples problem, has troubled mathematicians for decades. In the 1980s, Graham offered a prize of \$200,000 for anyone who could solve it. (He presented the challenge to one of the three computer scientists, Martin Heule of the University of Texas at Austin, last month.) The problem asks whether it is possible to colour each positive integer either red or blue, so that no proof of Fermat’s last theorem, and that satisfy Pythagoras’ famous equation $a^2 + b^2 = c^2$ are all the same colour. For example, for the Pythagorean triple 3, 4 and 5, if 3 and 5 were blue, 4 would have to be red. ▶

VIDEO

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NATURE PERSPECTIVE

Nature

The New York Times

World Takes On A.I. Putting A.I. in Charge A.I. and Hollywood Microsoft-OpenAI Partner

A.I. Is Coming for Mathematics, Too

For thousands of years, mathematicians have adapted to the latest advances in logic and reasoning. Are they ready for artificial intelligence?

New York Times

- [1] M. J. Heule and O. Kullmann, “The science of brute force,” *Communications of the ACM*, vol. 60, 2017.
- [2] M. J. Heule, O. Kullmann, and V. W. Marek, “Solving and verifying the boolean pythagorean triples problem via cube-and-conquer,” in *International Conference on Theory and Applications of Satisfiability Testing*, Springer, 2016.
- [3] M. Heule, “Schur number five,” in *Proceedings of the AAAI Conference on Artificial Intelligence*, vol. 32, 2018.

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now **simulate**, **verify**, and **synthesize** quantum circuits.

How can that possibly work?

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Spoiler: *one* Boolean encoding — going quantum only changes the **weights**.

SAT-based Quantum Circuit Simulation

Encoding Quantum Circuit Semantics in SAT

- ▶ SAT encoding of Clifford circuits [1]

✓scalable / ✗non-universal

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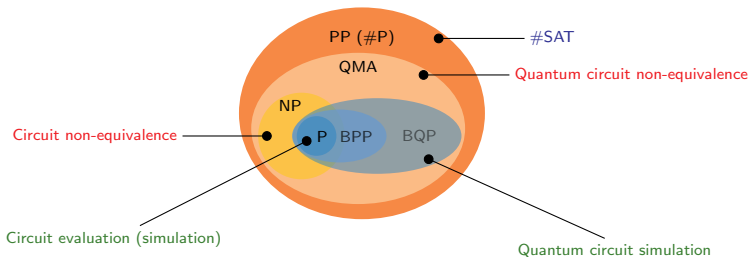
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Roadmap

- ▶ SAT and #SAT (model counting)
- ▶ Encoding quantum circuits in logic
- ▶ Application: Simulation, Equivalence checking, Verification, Synthesis
- ▶ Different encodings and performance

SAT = DPLL + CDCL

```
1: procedure DPLL( $\varphi$  in CNF)
2:   UnitPropagation( $\varphi$ )
3:   if  $\varphi$  is empty CNF then
4:     return SAT
5:   if empty clause in  $\varphi$  then
6:     return UNSAT
7:   Select  $x \in \text{vars}(\varphi)$ 
8:   Select  $v \in \{0, 1\}$ 
9:   if DPLL( $\varphi[x := v]$ ) then
10:    return SAT
11:  else
12:    return DPLL( $\varphi[x := 1 - v]$ )
```

[DPLL = Davis, Putnam, Logemann & Loveland]

Implication Graph

$$\{\overbrace{192}^{c_1}, \overbrace{13}^{c_2}, \overbrace{234}^{c_3}, \overbrace{45}^{c_4}, \overbrace{846}^{c_5}, \overbrace{56}^{c_6}, \dots\}$$

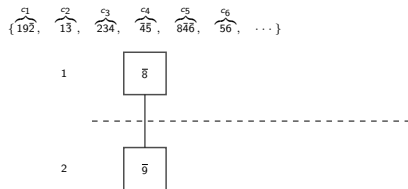
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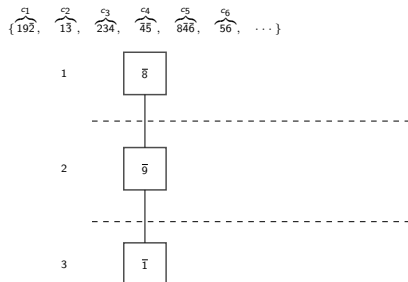
1

8

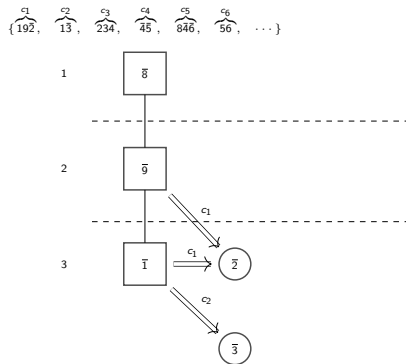
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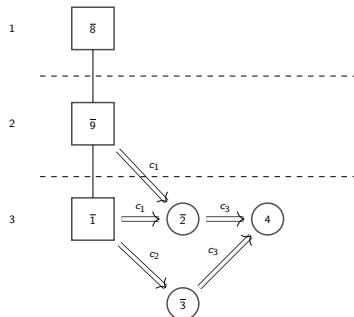


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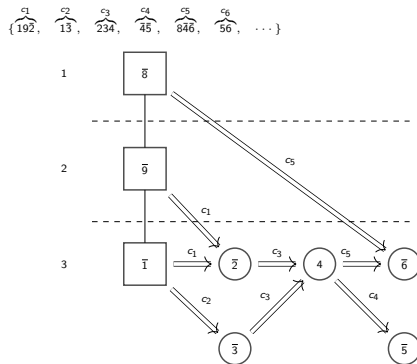


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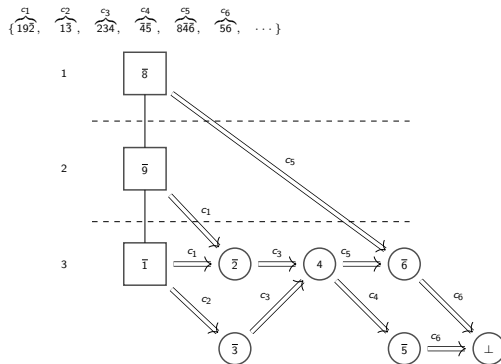
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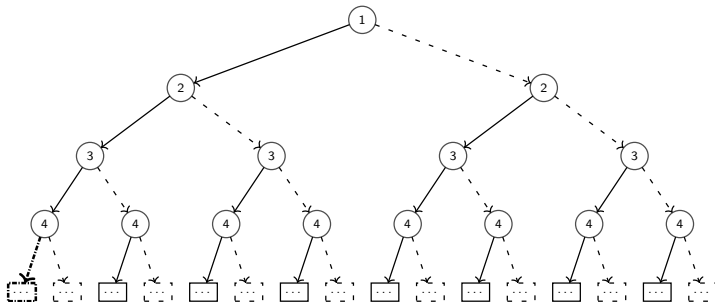
Implication Graph



We may 'learn' a clause $C = 5 \vee 6$ or $C = \bar{4} \vee 6$

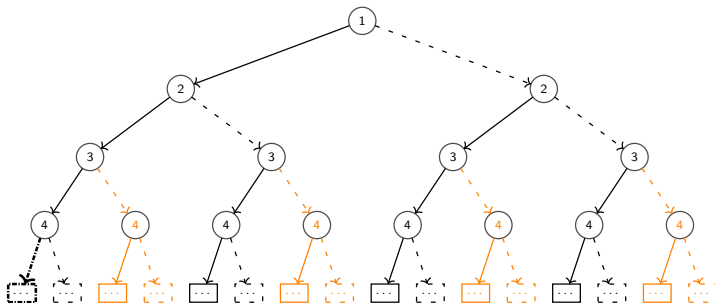
Constraint-Driven Clause Learning

$\varphi := \varphi \wedge c$ where c is a 'learned clause'



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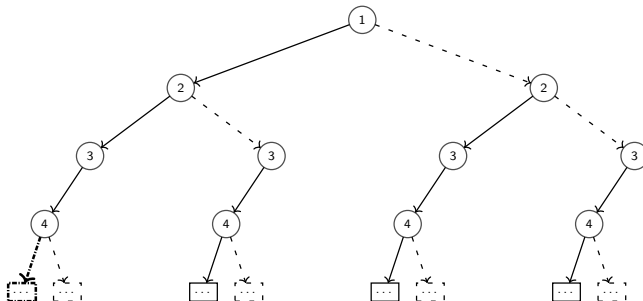
Proof theoretical results show exponential gains.

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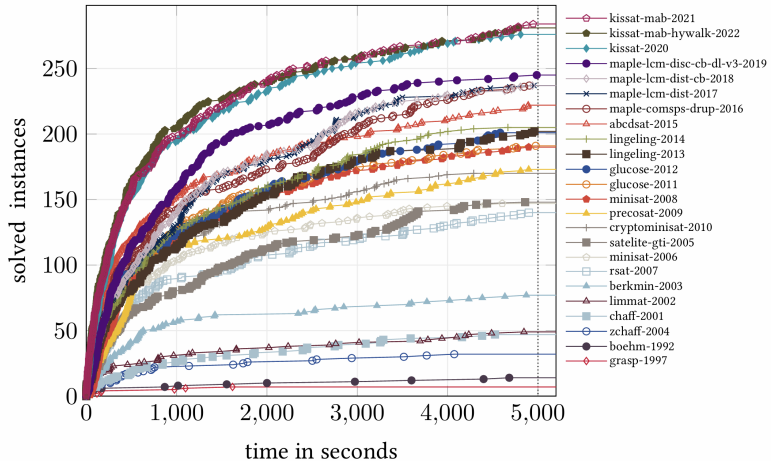


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SAT Solvers Are Fast in Practice



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Example (Weighted Model Counting)

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Satisfying assignments:

$SAT(F) = \{ \{ a \leftarrow 0, b \leftarrow 1 \}, \{ a \leftarrow 1, b \leftarrow 0 \}, \{ a \leftarrow 1, b \leftarrow 1 \} \} = \{ \bar{a}b, a\bar{b}, ab \}$

$\bar{a}\bar{b}$	—
$\bar{a}b$	$\frac{2}{3} \cdot 1$
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$$\text{ModelCount}_W(F) = \frac{4}{3} = \overbrace{\sum_{\vec{x} \in F^{-1}(1)}}^{\text{satisfying assignment}} \overbrace{\prod_{x_i \in \vec{x}} W(x_i)}^{\text{weight of assignment}}$$

Component caching

```
1: procedure #DPLL( $\varphi$ )
2:    $\varphi \leftarrow \text{UNITPROPAGATION}(\varphi)$ 
3:   if  $\varphi = \emptyset$  then
4:     return 1
5:   if  $\emptyset \in \varphi$  then
6:     return 0
7:   if CACHED( $\varphi$ ) then
8:     return LOOKUP( $\varphi$ )
9:   if  $\varphi = \varphi_1 \wedge \varphi_2$  with disjoint variables then
10:    return #DPLL( $\varphi_1$ )  $\cdot$  #DPLL( $\varphi_2$ )
11:   $x \leftarrow \text{SELECTVARIABLE}(\varphi)$ 
12:   $c \leftarrow \text{\#DPLL}(\varphi[x := 0]) + \text{\#DPLL}(\varphi[x := 1])$ 
13:  STORE( $\varphi, c$ )
14:  return  $c$ 
```

[1]

- [1] M. Thurley, "Sharpsat—counting models with advanced component caching and implicit bcp," in *International Conference on Theory and Applications of Satisfiability Testing*, Springer, 2006.

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Encoding quantum states as Boolean formula

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$$|0\rangle + |1\rangle \quad \{\{x \leftarrow 0\}, \{x \leftarrow 1\}\}$$

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$$\frac{1}{\sqrt{2}} |0\rangle + \frac{1}{\sqrt{2}} |1\rangle \quad \left\{ \{x \leftarrow 0, \text{"weight } \frac{1}{\sqrt{2}}\}, \{x \leftarrow 1, \text{"weight } \frac{1}{\sqrt{2}}\} \right\}$$

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$$W(z) = \begin{cases} \frac{1}{\sqrt{2}} & \text{if } z = \bar{h} \\ -\frac{1}{\sqrt{2}} & \text{if } z = h \\ 1 & \text{if } z \text{ represents any other literal} \end{cases}$$

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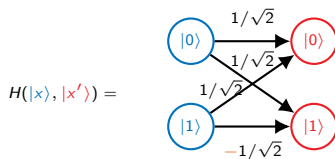
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Encoding quantum gates

Goal: find formula $F_H(x, x')$,) for the gate $H = \frac{1}{\sqrt{2}} \begin{bmatrix} 1 & 1 \\ 1 & -1 \end{bmatrix}$.

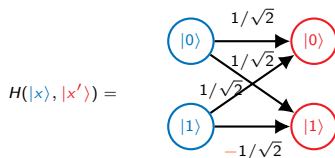
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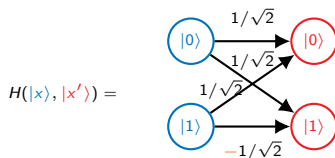
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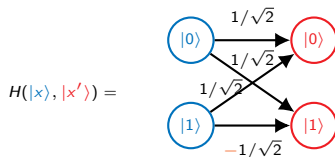


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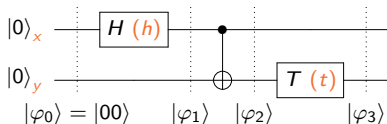
(For $H, CNOT$ and T is sufficient as these are universal; Solovay-Kitaev Thm)

Sequential and parallel composition of gates

Using F_H, F_{CNOT}, F_T for encoding the output state of a circuit C :

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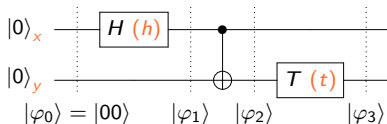
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where x, y, h and t are Boolean variables.

Sequential and parallel composition of gates

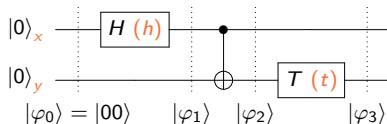
Using F_H, F_{CNOT}, F_T for encoding the output state of a circuit C :



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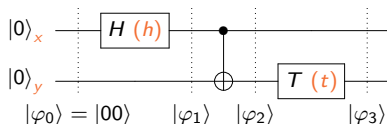


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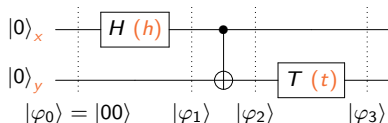


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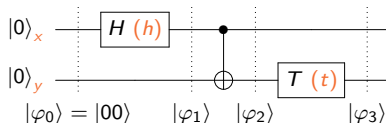


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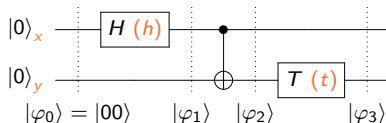


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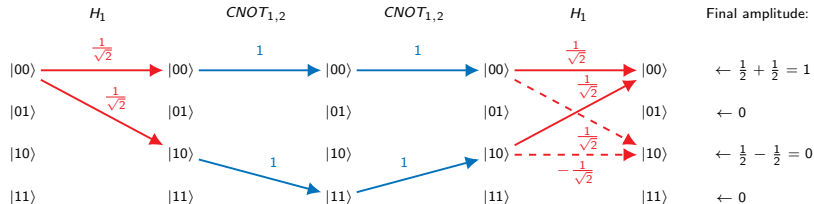
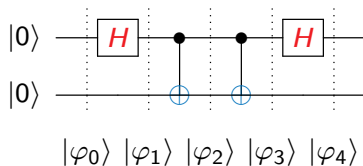


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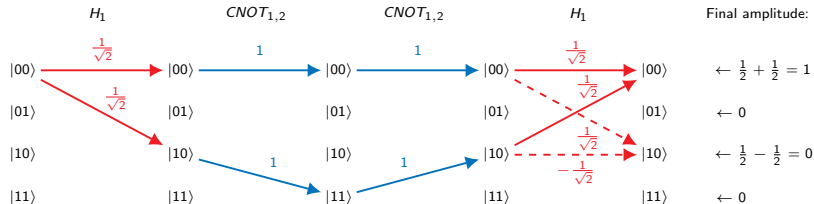
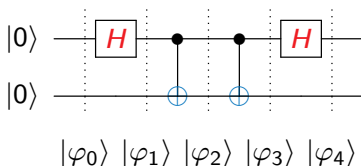
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$$\#SAT_W(F_C \wedge x_3 = a \wedge y_3 = b) = \langle ab | \varphi_3 \rangle$$

The Model Counter Computer Path Sums



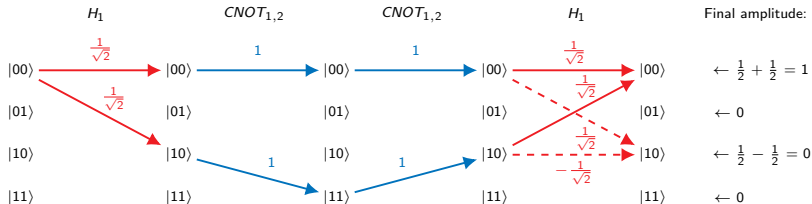
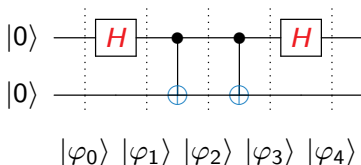
The Model Counter Computer Path Sums



In WMC, the circuit above has four satisfying assignments:

$$\begin{aligned}
 \{h' \bar{h} x_1 \bar{y}_1 x_2 y_2 x_3 \bar{y}_3 x_4 \bar{y}_4 &\equiv -\frac{1}{2} |10\rangle, & \bar{h}' \bar{h} x_1 \bar{y}_1 x_2 y_2 x_3 \bar{y}_3 \bar{x}_4 \bar{y}_4 &\equiv \frac{1}{2} |00\rangle, \\
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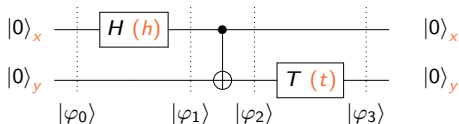
Each satisfying assignment corresponds to a path! So WMC computes the path sum.

Roadmap

- ▶ SAT and #SAT (model counting)
- ▶ Encoding quantum circuits in logic
- ▶ Application: Simulation, Equivalence checking, Verification, Synthesis
- ▶ Different encodings and performance

Simulation

Given an n -qubit quantum circuit C and a computational-basis measurement specification, which is described by a state $|\vec{b}\rangle$ on n qubits, compute the probability of measuring $|\vec{b}\rangle$ in state $C|0\dots 0\rangle$, i.e. $\langle \vec{b} | C | 0\dots 0 \rangle$.

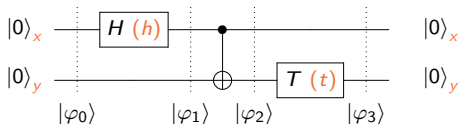


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We support general observables O by encoding $\langle 0 | C^\dagger O C | 0 \rangle$.

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Quantum Hoare Logic [1]

Given a circuit C , projectors P and Q , the Hoare triple $\{P\} C \{Q\}$ is true if and only if for any input state $|\psi\rangle$, we have

$$|\psi\rangle \models P \Rightarrow C |\psi\rangle \models Q,$$

where $|\psi\rangle \models P$ iff $P |\psi\rangle = |\psi\rangle$.

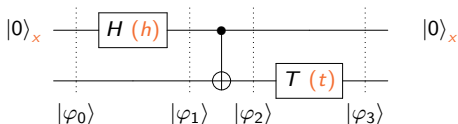
Verification

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- [1] M. Ying, "Floyd–Hoare logic for quantum programs," *ACM Transactions on Programming Languages and Systems (TOPLAS)*, vol. 33, 2012.

(Approximate) Quantum Circuit Equivalence Checking

Definition (Approximate equivalence checking)

Let U and V be two n -qubit quantum circuits, Then U and V are ϵ -equivalent, written as $U \simeq_{\epsilon} V$, if and only if the **Jamiolkowski fidelity** between U and V is not less than $1 - \epsilon$ ($\epsilon \in [0, 1]$).

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We call this the “cyclic encoding” since it equals $F_{UV^{\dagger}}(\vec{q}_0, \dots, \vec{q}_0)$

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Quantum circuit synthesis

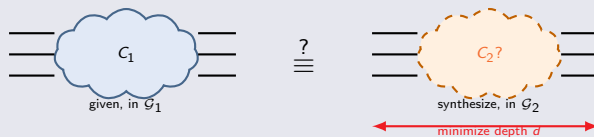
The synthesis problem

Inputs:

- ▶ A circuit C_1 is gateset $\mathcal{G}_1$
- ▶ A gate set $\mathcal{G}_2$

Output:

- ▶ A **minimum-depth circuit** C_2 in gateset $\mathcal{G}_2$ that is equivalent to C_1



Quantum circuit synthesis

The approximate synthesis problem

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Approach

quantum circuit synthesis $\rightarrow$ maximum (weighted) model counting [1]

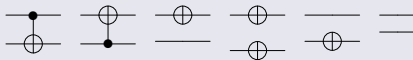
[1] D. Fremont, M. Rabe, and S. Seshia, "Maximum model counting," in *AAAI*, vol. 31, 2017.

Synthesis = Max#SAT over “which gate where”

Input $\mathcal{C}_1$ is given as $F_{\mathcal{C}_1}(\vec{x}, \vec{x}')$.

1. Encode one generic layer $F_{\mathcal{L}}(\vec{x}, \vec{x}', p)$ — selectors p choose the gate

e.g., for $p \in [P]$, the circuit $\mathcal{L}_p$ is:

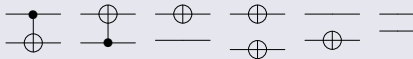


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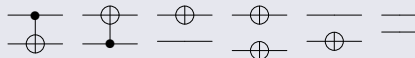
2. Stack d layers $\mathcal{C}_{2,d} = \underbrace{\mathcal{L} \cdots \mathcal{L}}_{d \times}$, check $\mathcal{C}_{2,d} \equiv_{\epsilon} \mathcal{C}_1$, and **maximise**

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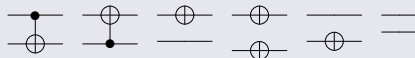
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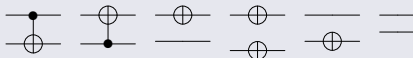
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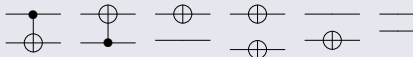
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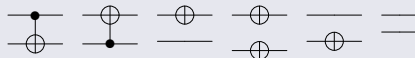
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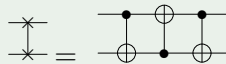
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Exactly Synthesise SWAP from $\{CX\}$

$d=3$: count = $2^2 = 4$,
 $C_2 = (CX_{01}, CX_{10}, CX_{01})$.



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All the other gates: negative & complex weights

Gate	Relation F_G	Weights
$X, CX, Toffoli$	Boolean	none
H	$h \Leftrightarrow (x \wedge x')$	$W(h) = -\frac{1}{\sqrt{2}}$
T	$(x \Leftrightarrow x') \wedge (w \Leftrightarrow x)$	$W(w) = \omega = \frac{1+i}{\sqrt{2}}$
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All the other gates: negative & complex weights

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$H, Toffoli$ suffice (universal, Solovay–Kitaev [1]).

Irrational H needs approximation [2].

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Different encoding in the Pauli basis (entanglement spectrum).

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Different encodings

In the Pauli basis: $\rho = \sum_j \beta_j \cdot P_j$ for $P_j \in \{I, X, Y, Z\}^{\otimes n}$ for $\beta_j \in \mathbb{R}$,

We reserve two Boolean variables (x, z) for a Pauli, so e.g.:

$$Z_1, Z_2 \equiv \bar{x}_1 \wedge \bar{x}_2 \wedge (z_1 \oplus z_2)$$

Quokka#: <https://github.com/System-Verification-Lab/Quokka-Sharp>

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	Nr of qubit variables	Gate encoding length	weights
Pauli basis	$2n$	m	real (+negative)
Computational basis	n	$2m$	complex

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Encoding equivalence checking for n qubits:

Encoding Basis	Eq. Encoding	Performance
Pauli basis	Cyclic	+/-
Computational basis	Cyclic	++
Pauli basis	Choi iso. trick	++

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Practical performance

We can use any state-of-the-art weighted model counting **which supports negative weights**, such as Ganak [1]!

Random-circuit simulation comparison [2]:

- ▶ solid line: WMC
- ▶ dashed line: ZX graphical-calculus based simulator

[1] S. Sharma, S. Roy, M. Soos, and K. S. Meel, "Ganak: ...," in *IJCAI*, vol. 19, 2019.

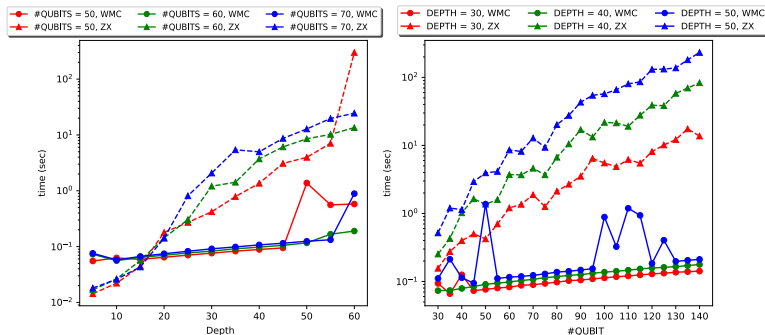
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Does synthesis work?

Depth-optimal TOFFOLI synthesis into $\{H, CX, C\sqrt{X}\}$

$$\text{Toffoli} = C\sqrt{X}_{0,2}C\sqrt{X}_{1,2}CX_{0,1}C\sqrt{X}^\dagger_{0,2}CX_{0,1}$$

iteration = depth	1	2	3	4	5	6
Clauses	136	992	1886	2780	3674	4568
Literals	524	3760	7126	10492	13858	17224
runtime (s)	0.03	0.04	0.16	3.39	141.86	577.78

converges to the optimal 6-layer circuit.

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Comparison with state-of-the-art mitms

$$CS = CX_{1,0} T_0^\dagger CX_{1,0} T_0 T_1$$

depth d	3	4	5	6	7	8
mitms [2]	0.19	1.60	12.4	84.6	TO	TO
Quokka#	0.05	0.27	2.34	6.47	197	2354

reaches depths 7–8 where mitms times out.

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Takeaway

Quantum circuit synthesis $\longrightarrow$ Maximum weighted model counting

- ▶ New SAT-based methods for QC $\leftrightarrow$ new applications for solvers.
- ▶ Approximate equivance (QMA) and synthesis with exact symbolic tool ($\#$ SAT).

Analyze your quantum circuits with Quokka#:

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- ▶ Approximate equivalence (QMA) and synthesis with exact symbolic tool ($\#$ SAT).

Open problems

- ▶ Approximation algorithms for $\#$ SAT with negative weights.
- ▶ Support for sampling and noise.
- ▶ Push synthesis well beyond small qubit count and depth.
- ▶ Push open problems in Q. foundations: contextuality, MUB, non-locality,

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